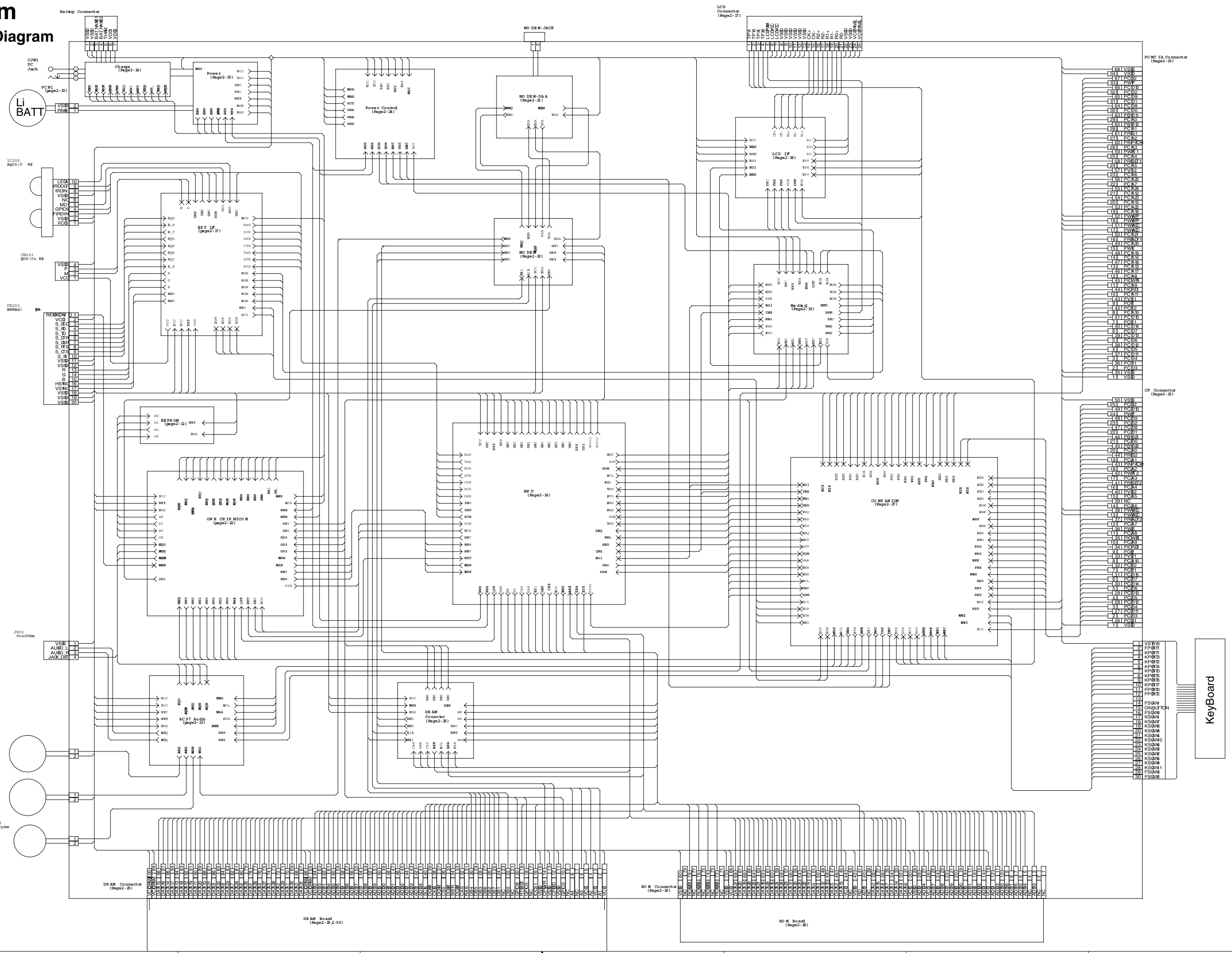
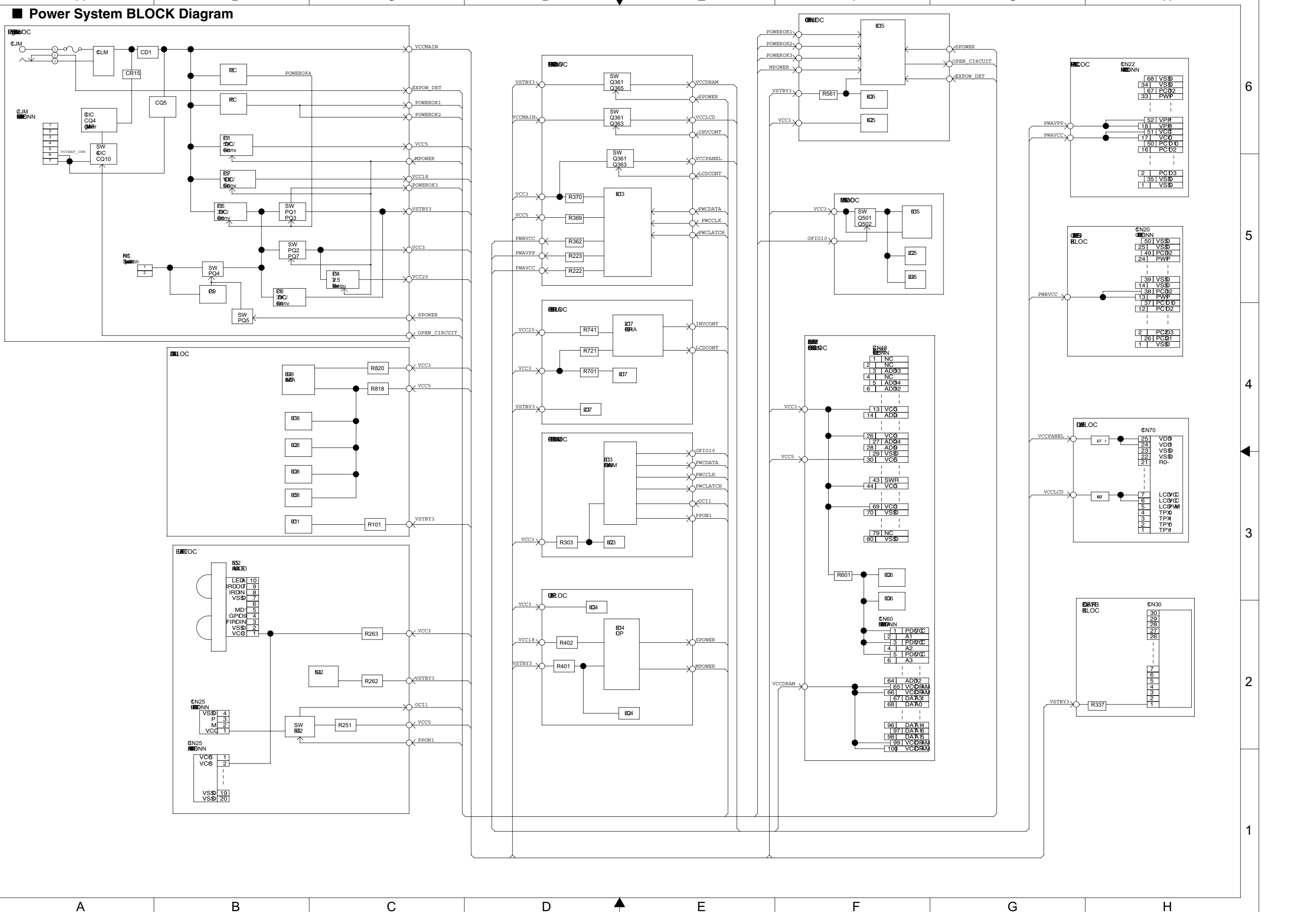


BLOCK Diagram

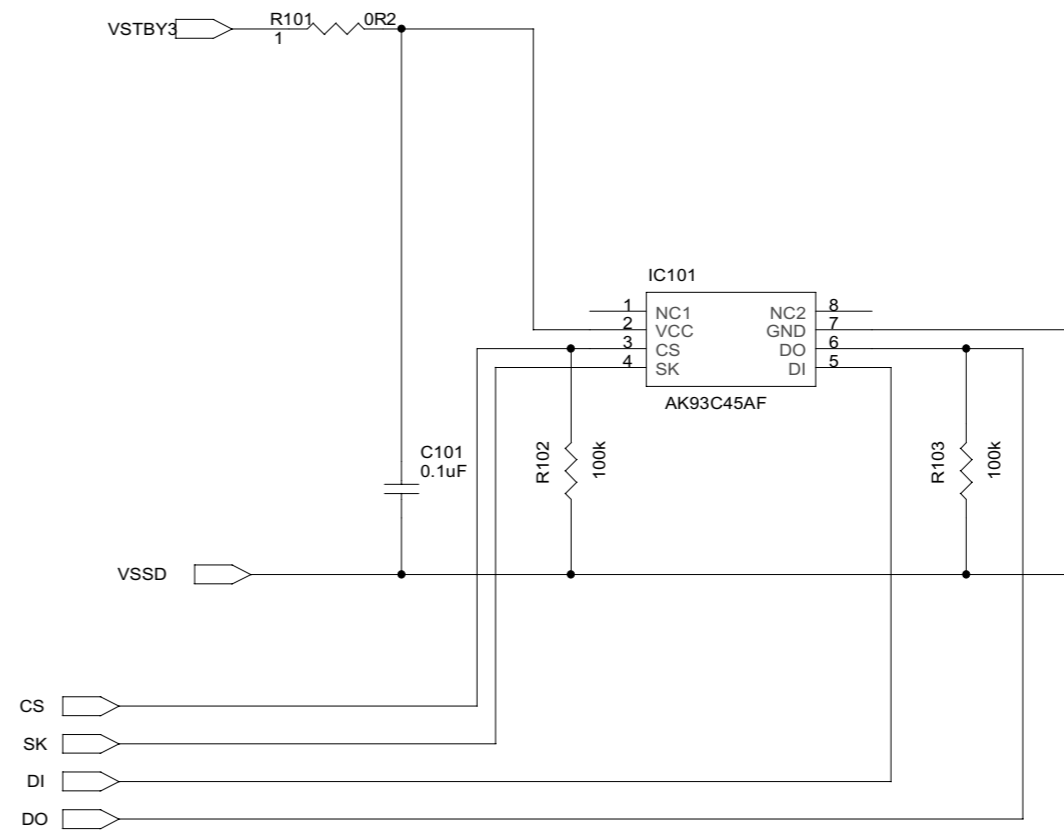
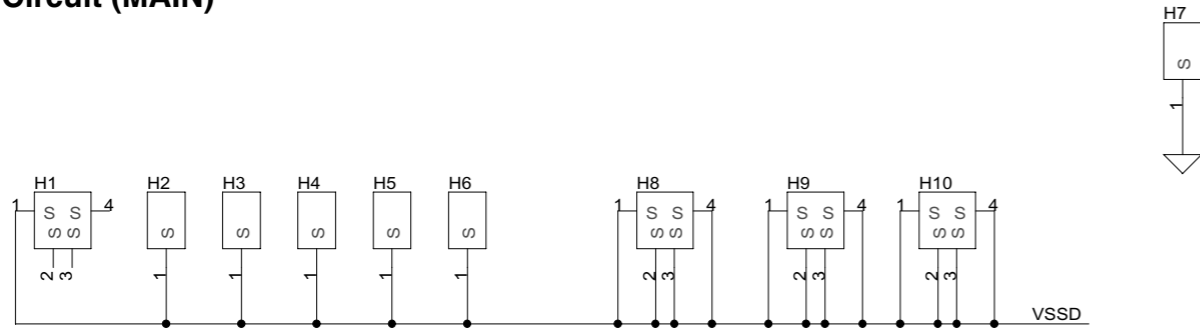
System BLOCK Diagram



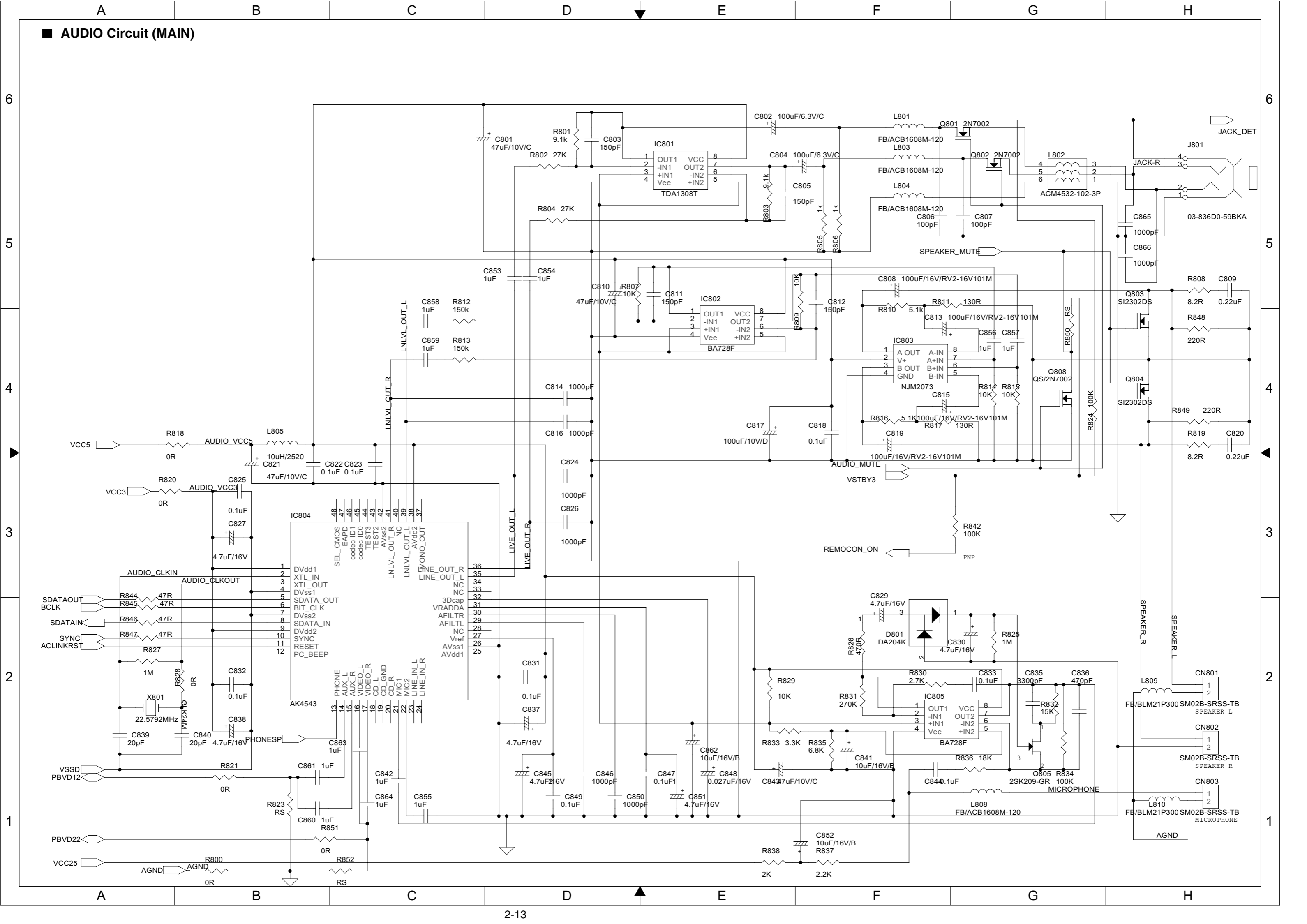


Standard schematic diagrams

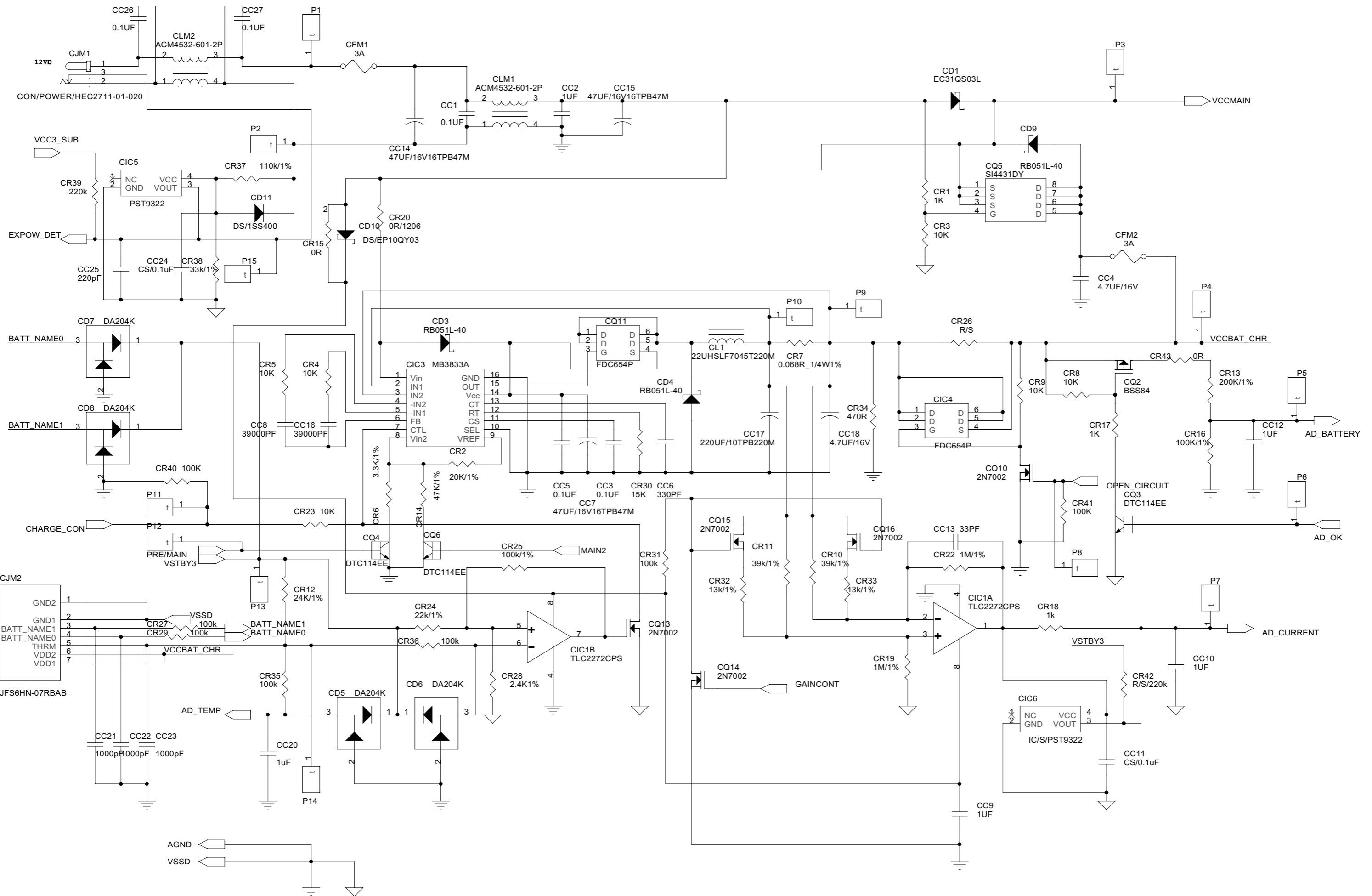
■ EEPROM Circuit (MAIN)



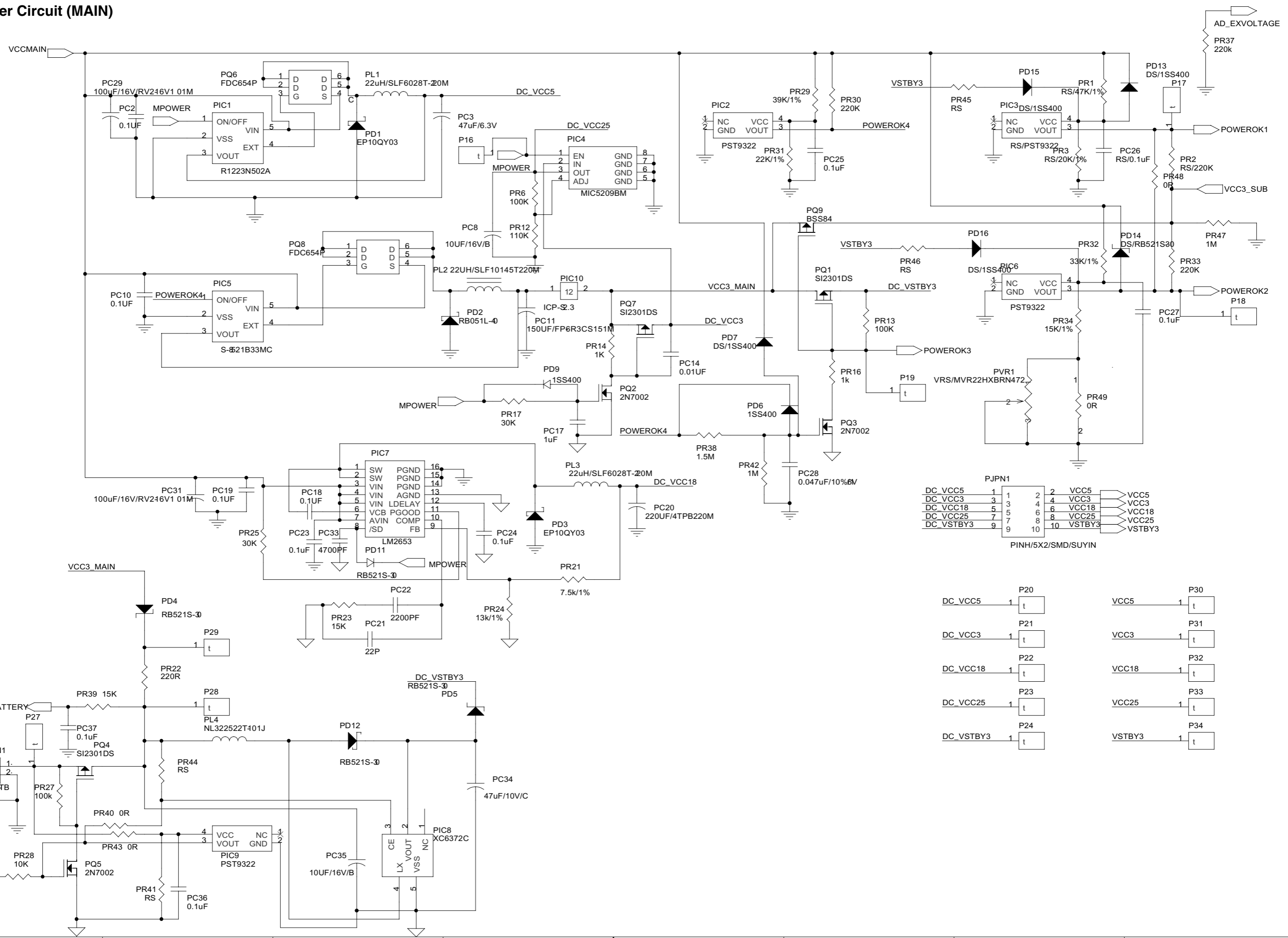
AUDIO Circuit (MAIN)



Charger Circuit (MAIN)



Power Circuit (MAIN)



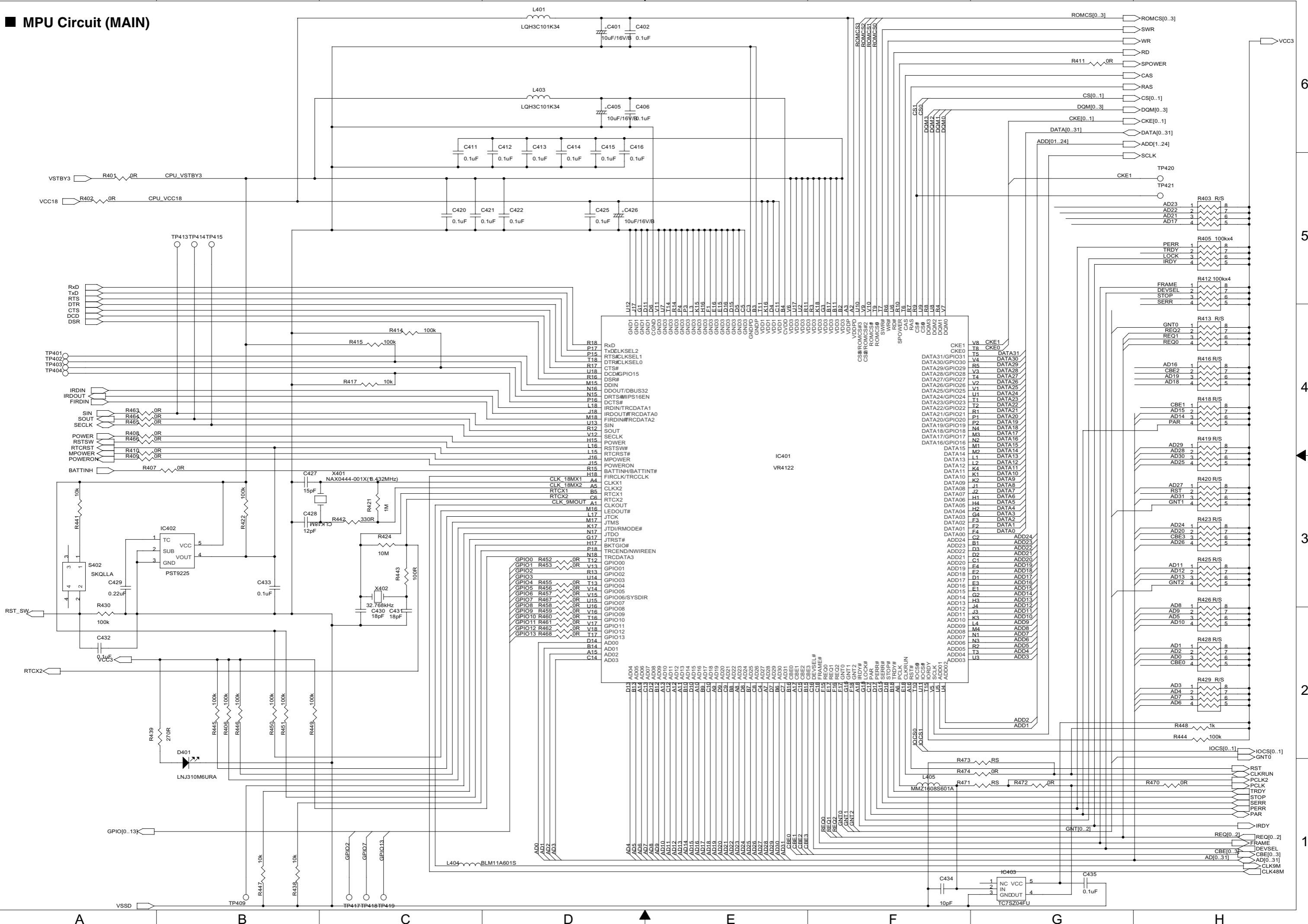
PJPN1

DC_VCC5	1	2	VCC5
DC_VCC3	3	4	VCC3
DC_VCC18	5	6	VCC18
DC_VCC25	7	8	VCC25
DC_VSTBY3	9	10	VSTBY3

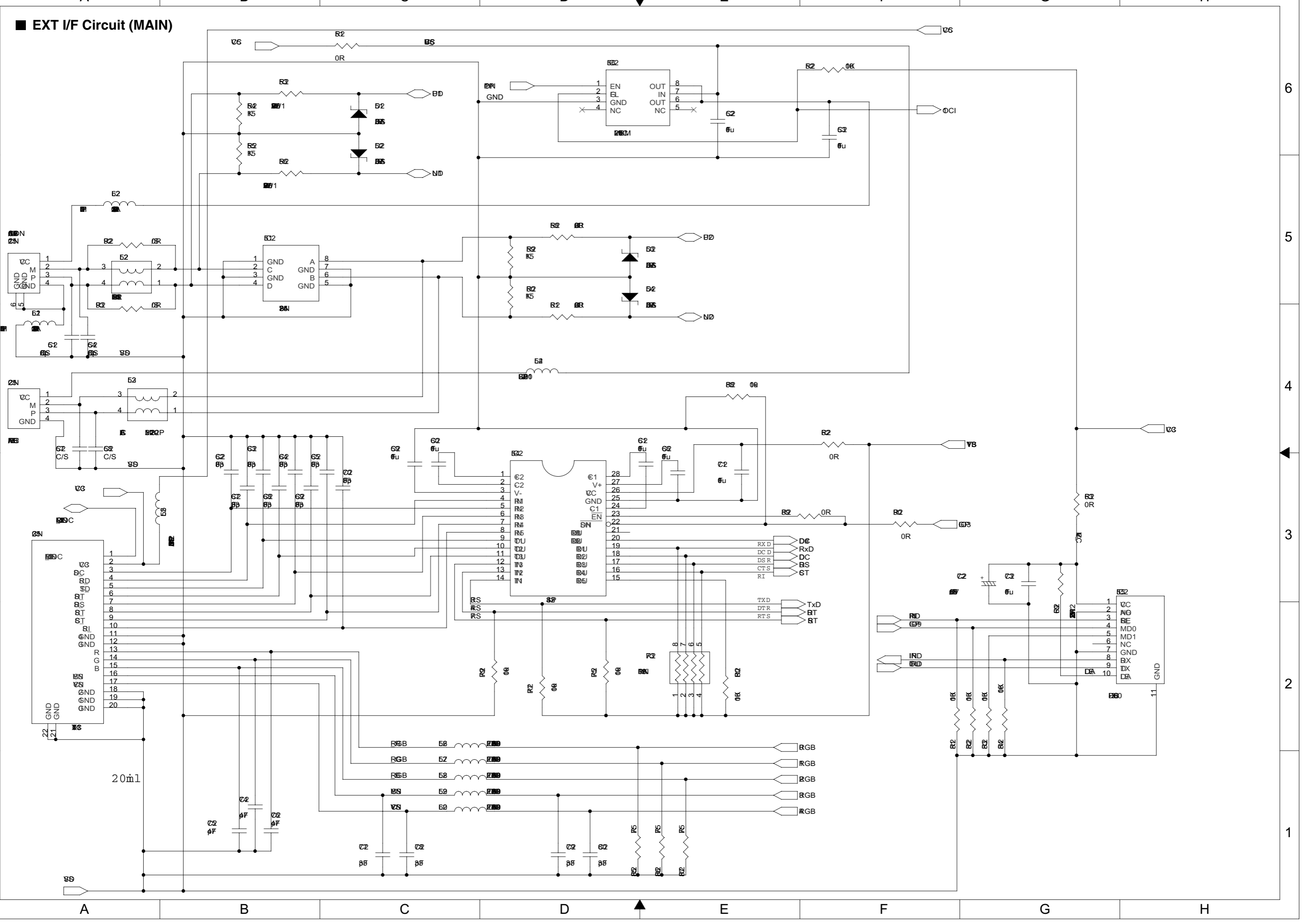
PINH/5X2/SMD/SUYIN

DC_VCC5	P20	VCC5	P30
DC_VCC3	P21	VCC3	P31
DC_VCC18	P22	VCC18	P32
DC_VCC25	P23	VCC25	P33
DC_VSTBY3	P24	VSTBY3	P34

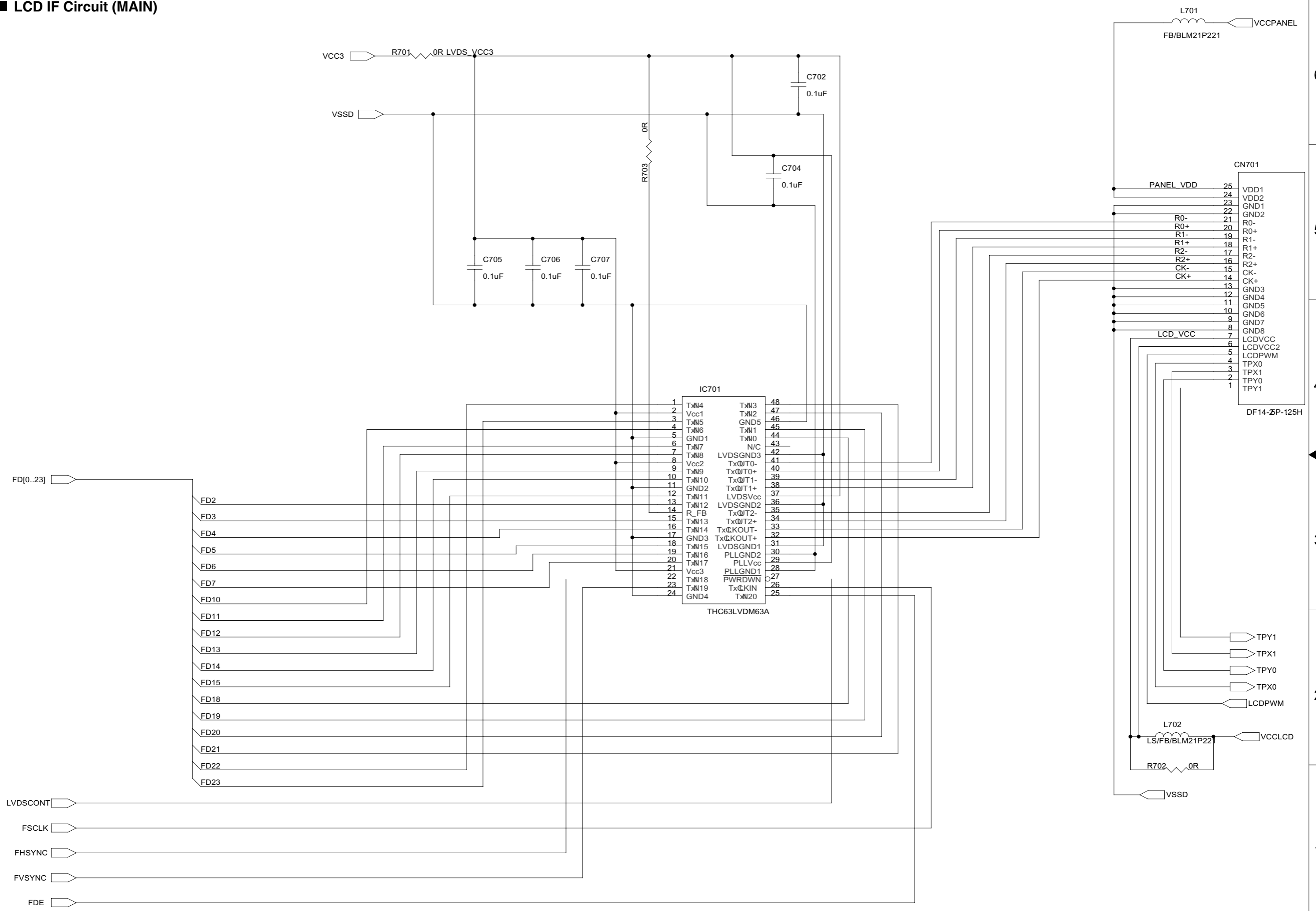
MPU Circuit (MAIN)



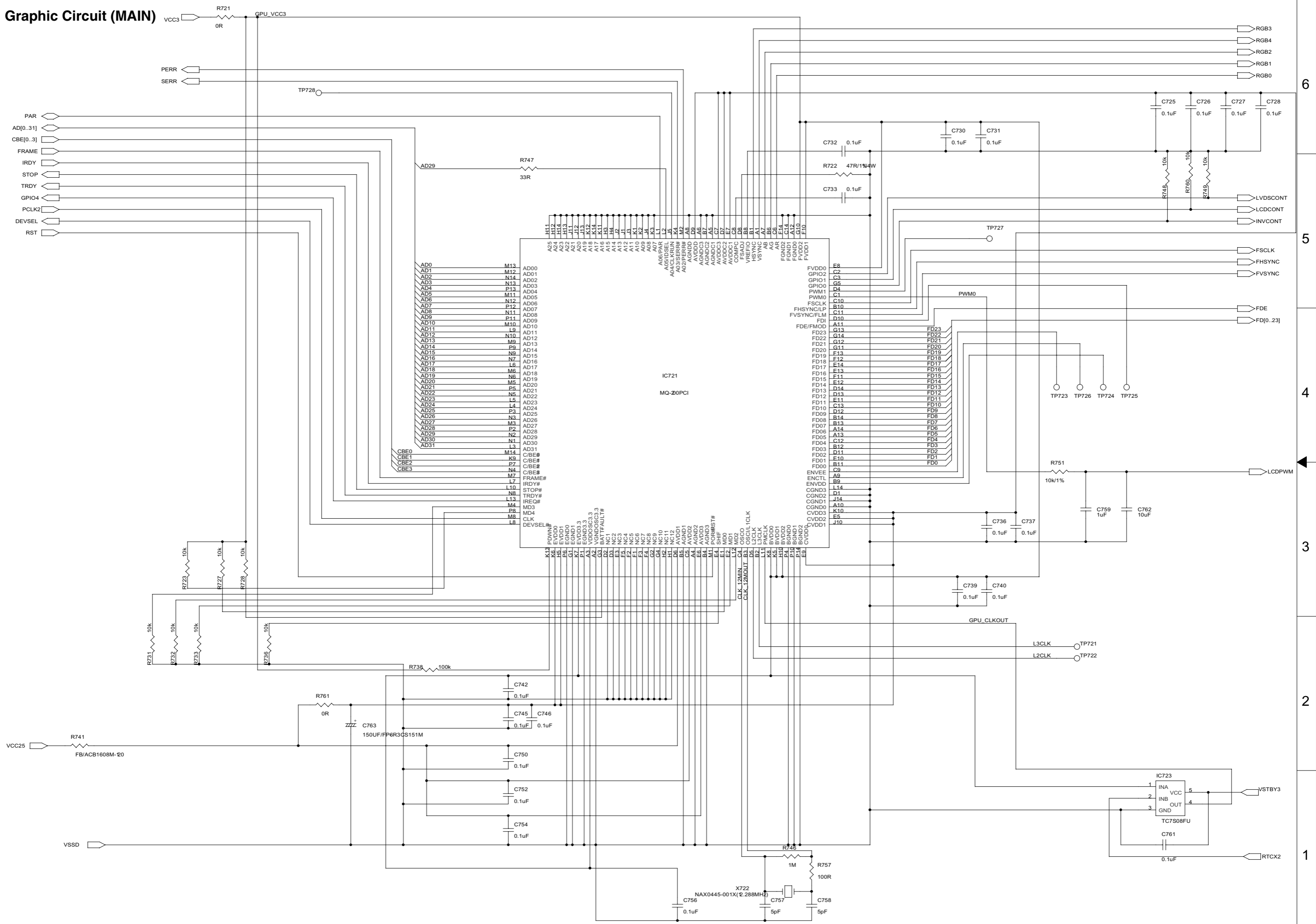
■ EXT I/F Circuit (MAIN)



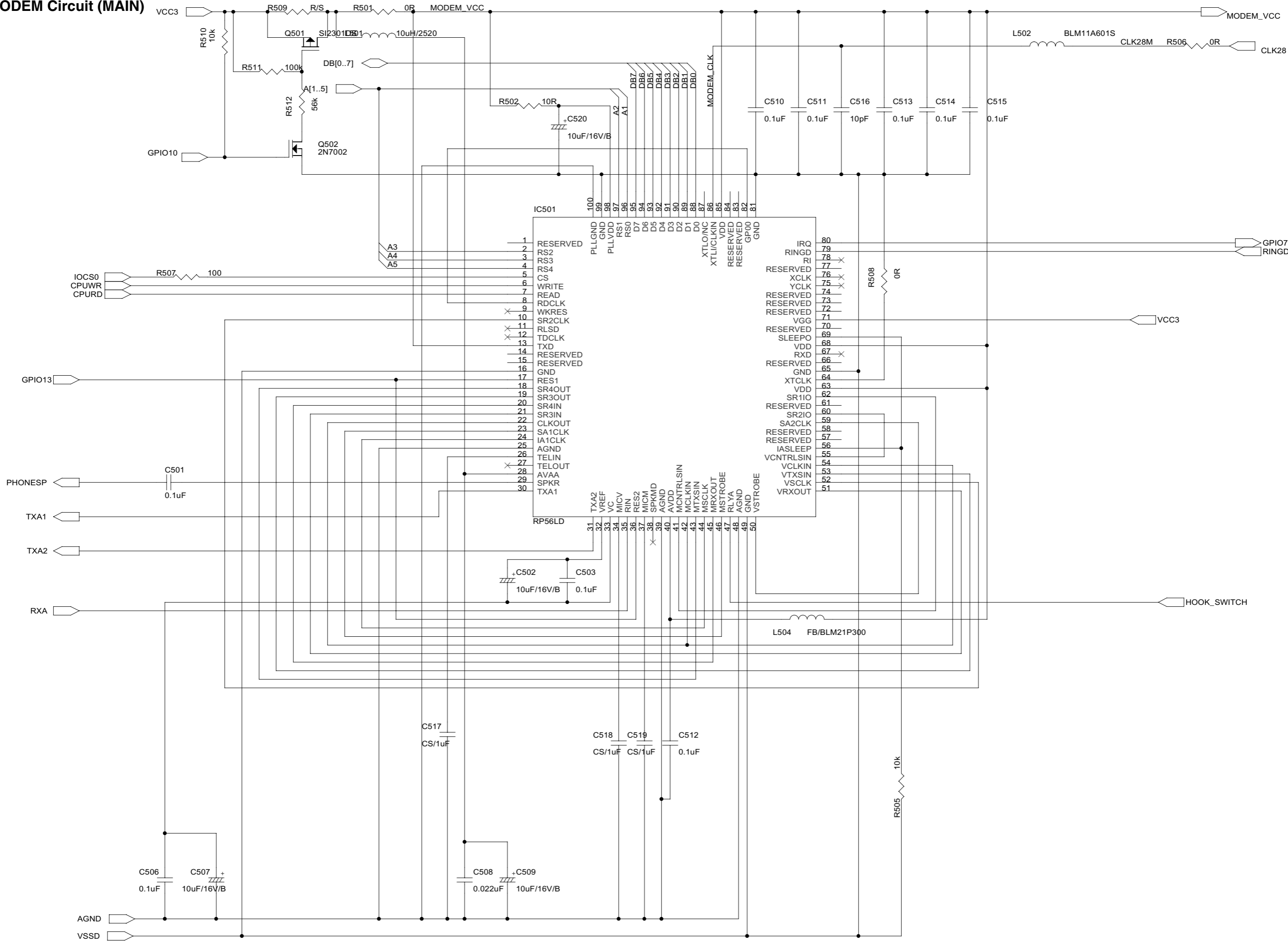
■ LCD IF Circuit (MAIN)



Graphic Circuit (MAIN)



MODEM Circuit (MAIN)

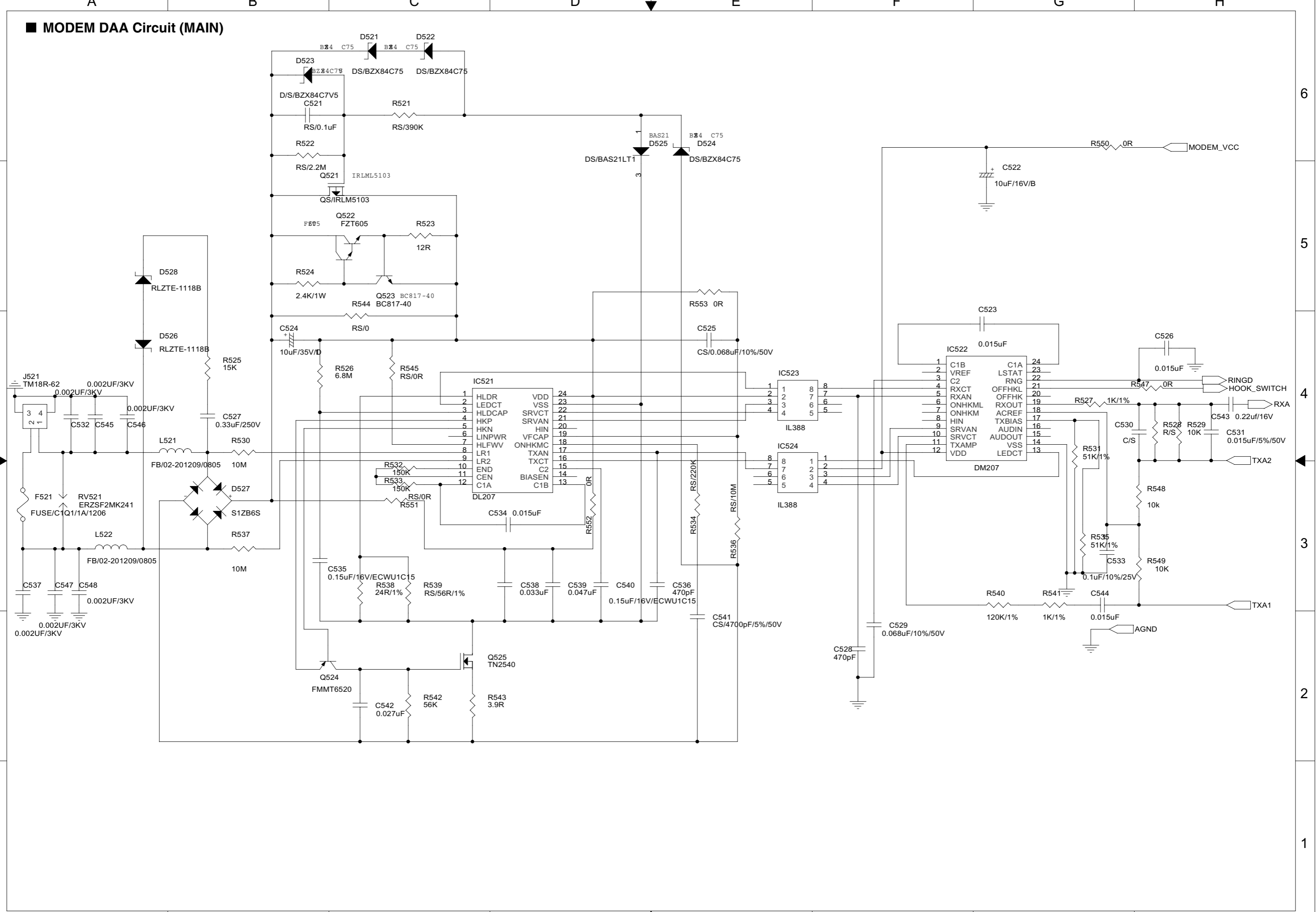


MODEM DAA Circuit (MAIN)

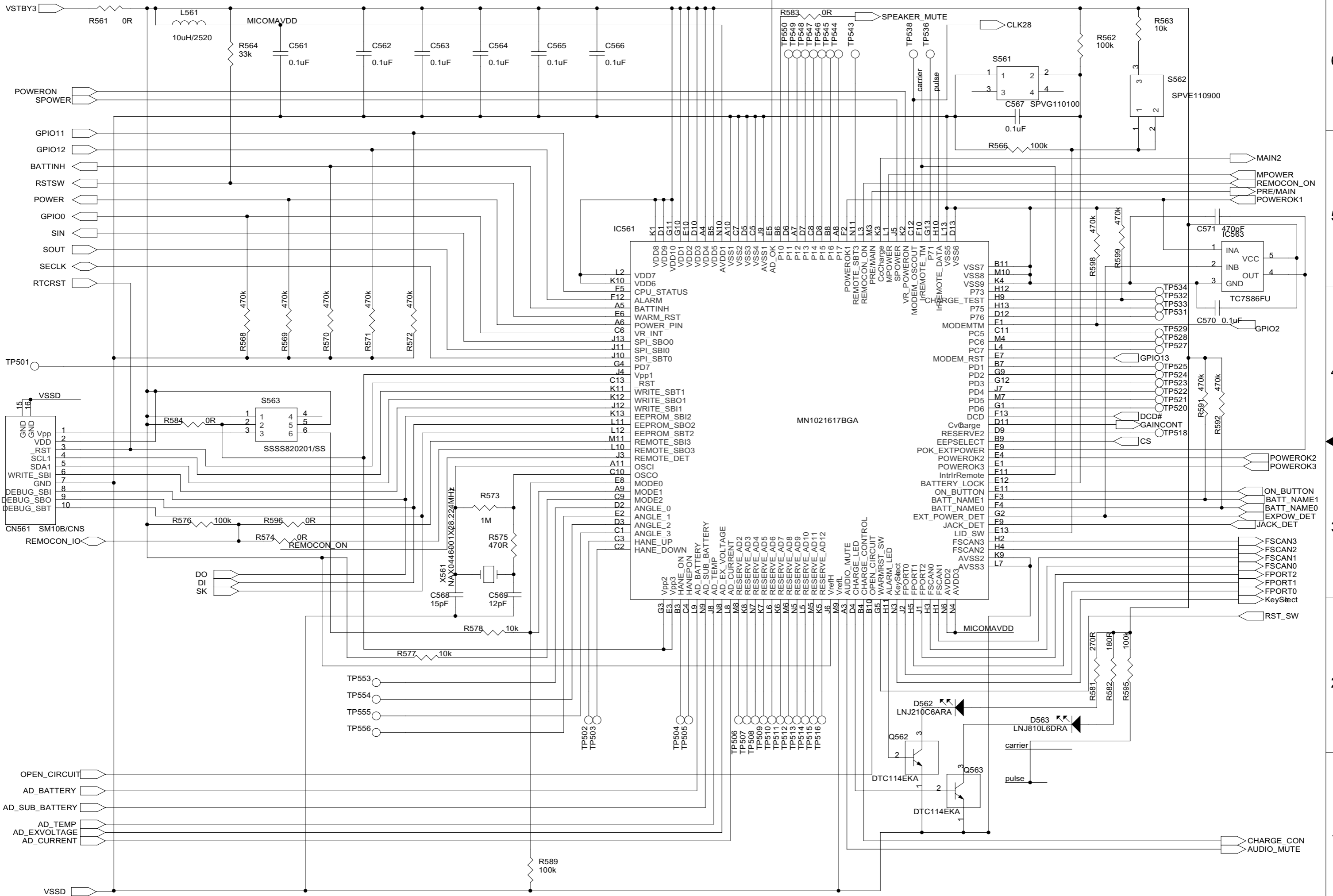
6
5
4
3
2
1

6
5
4
3
2
1

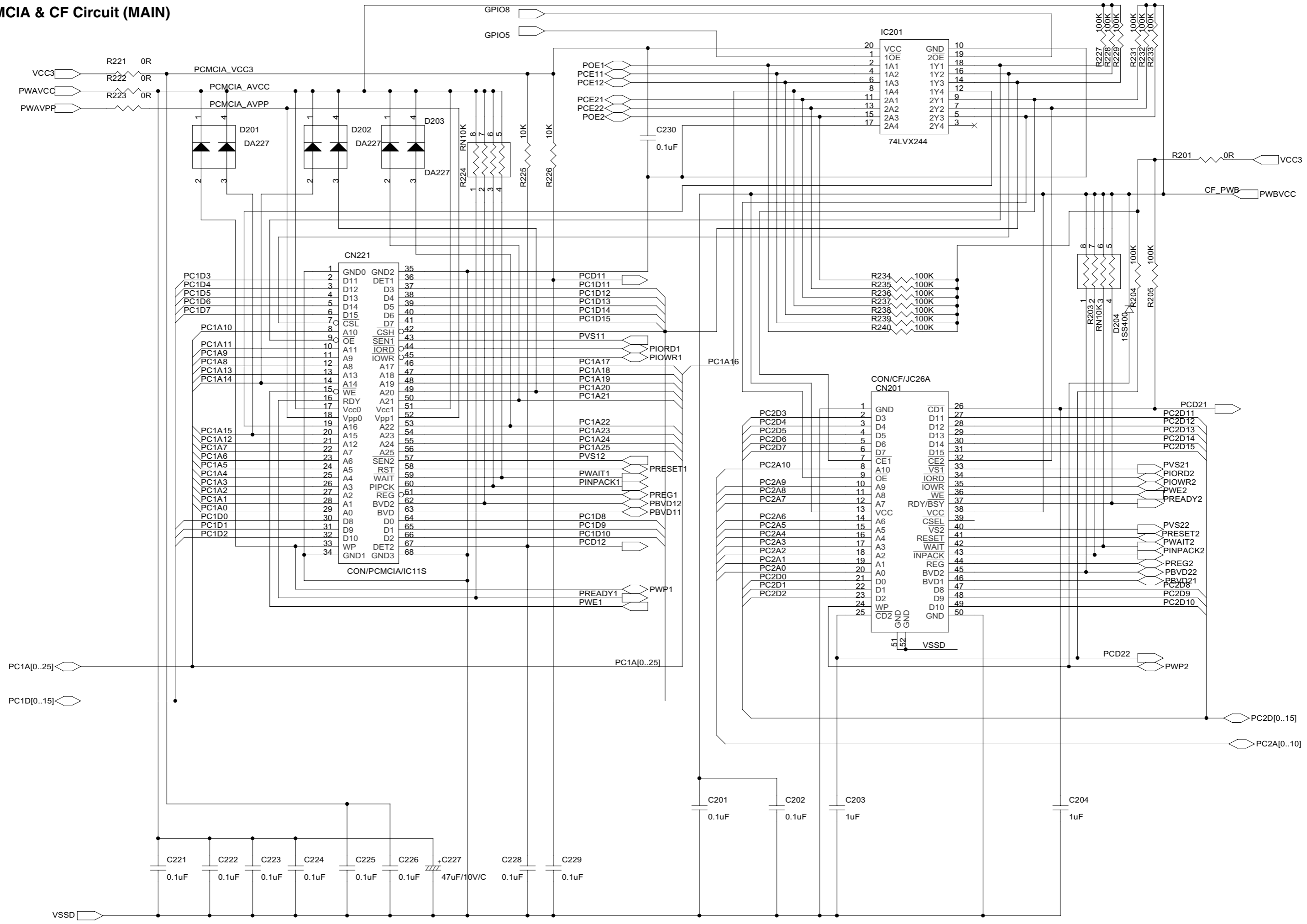
A B C D E F G H



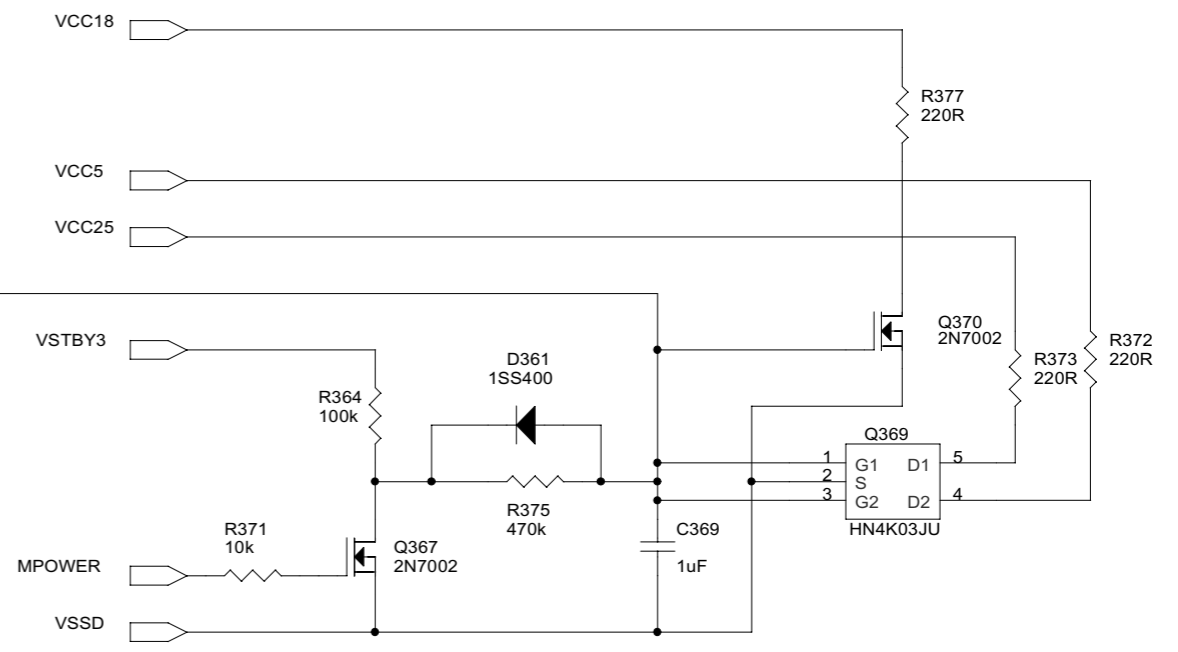
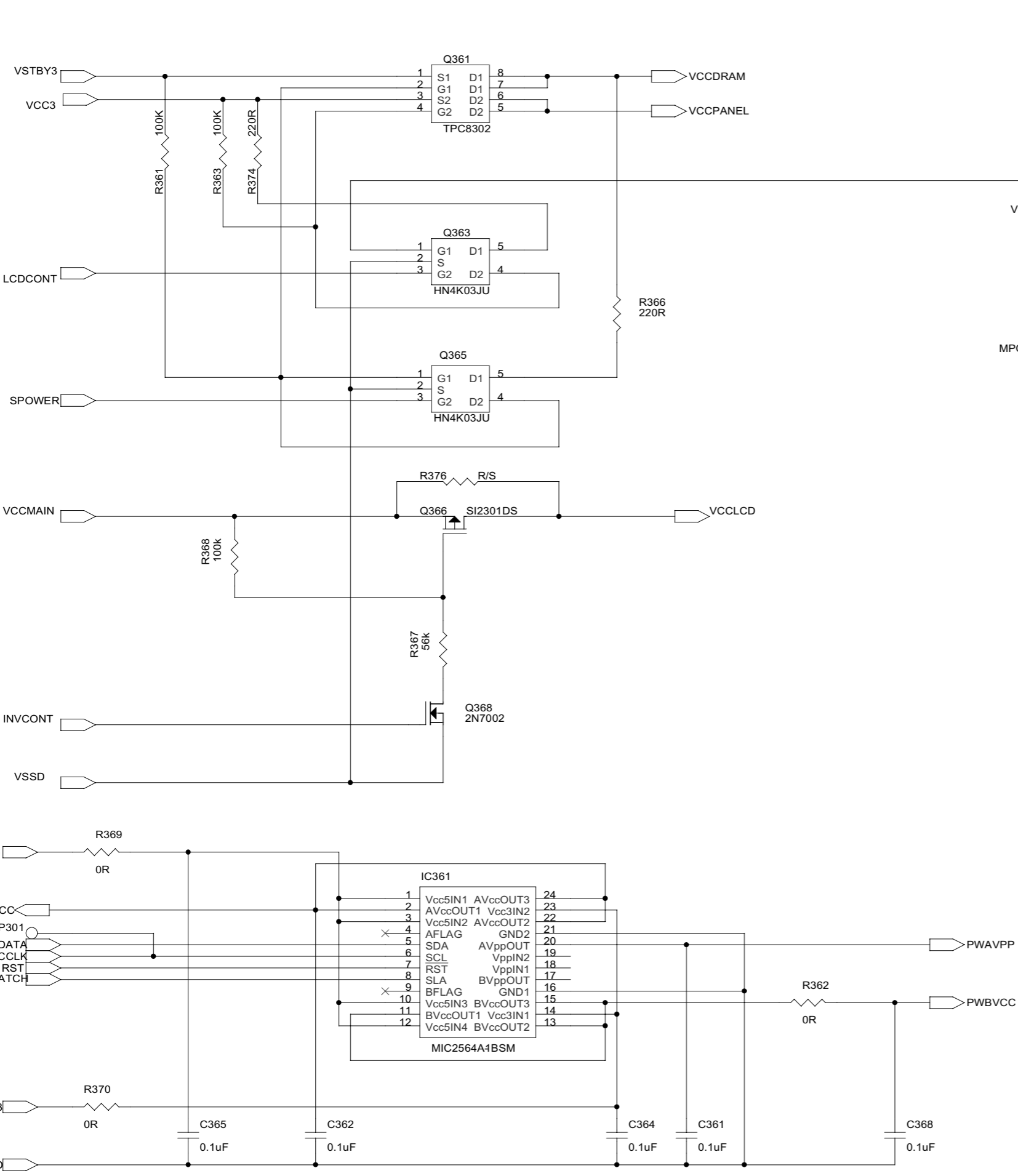
ONECHIP Circuit (MAIN)



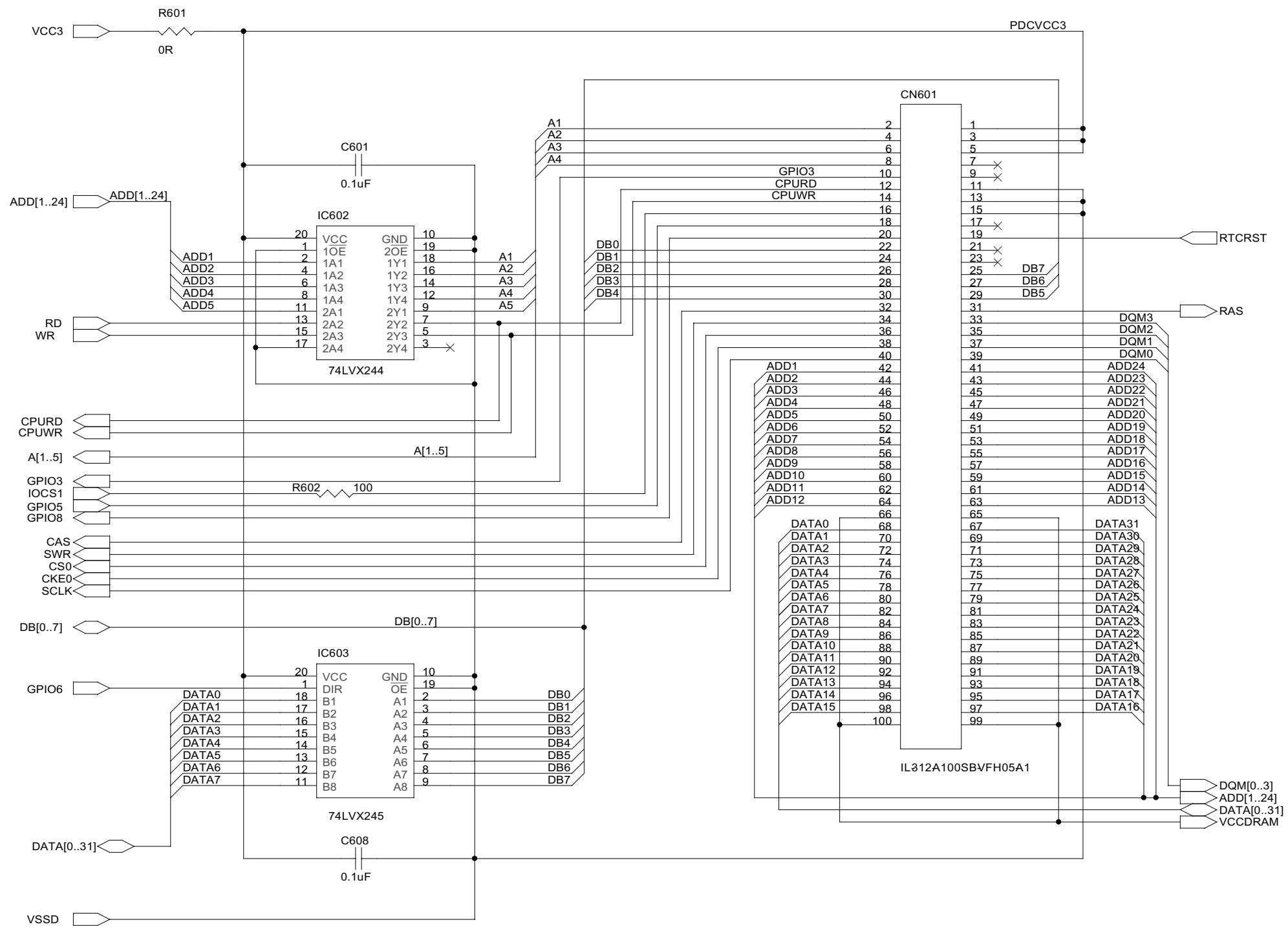
PCMCIA & CF Circuit (MAIN)



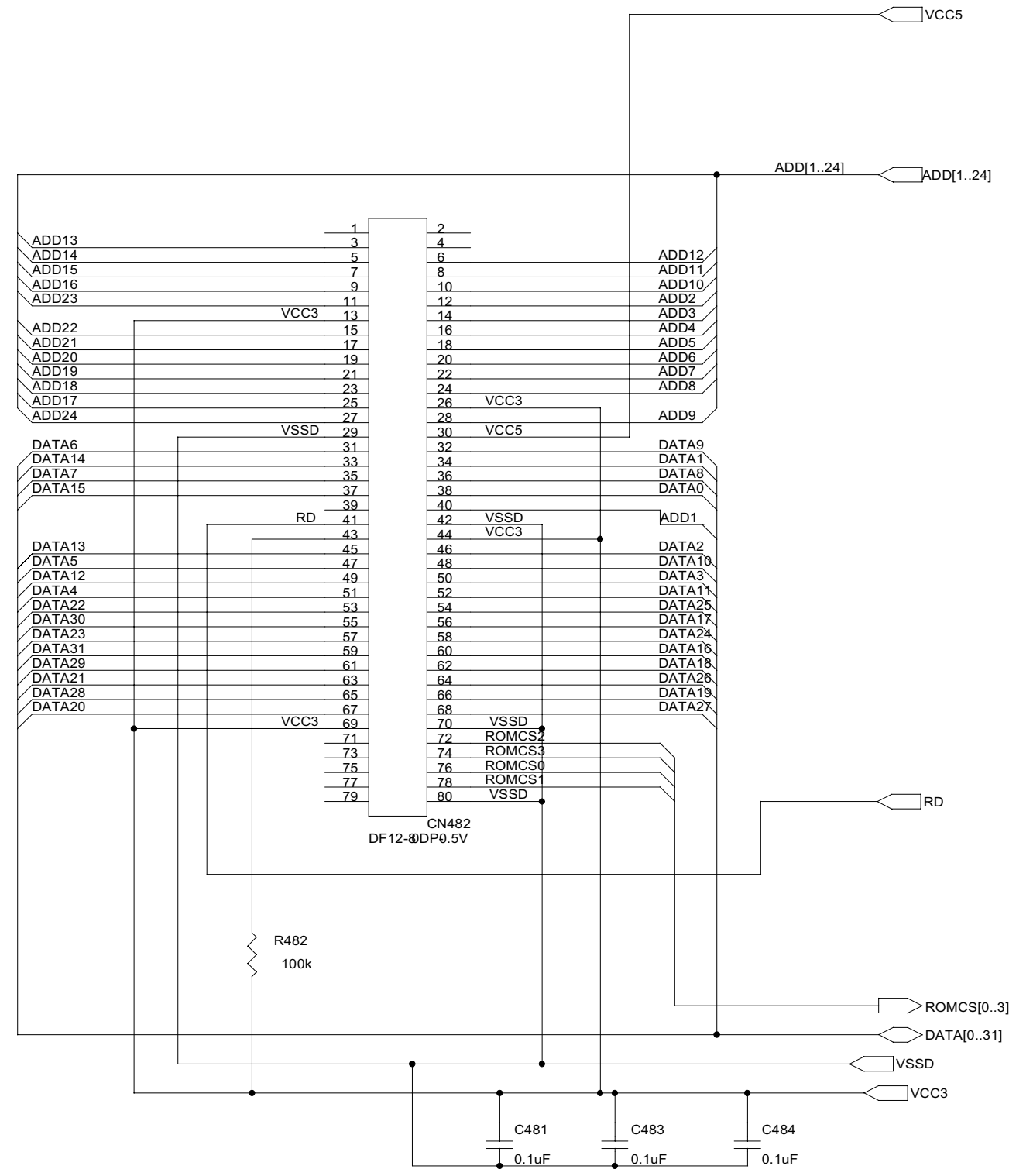
POWER CONTROL Circuit (MAIN)



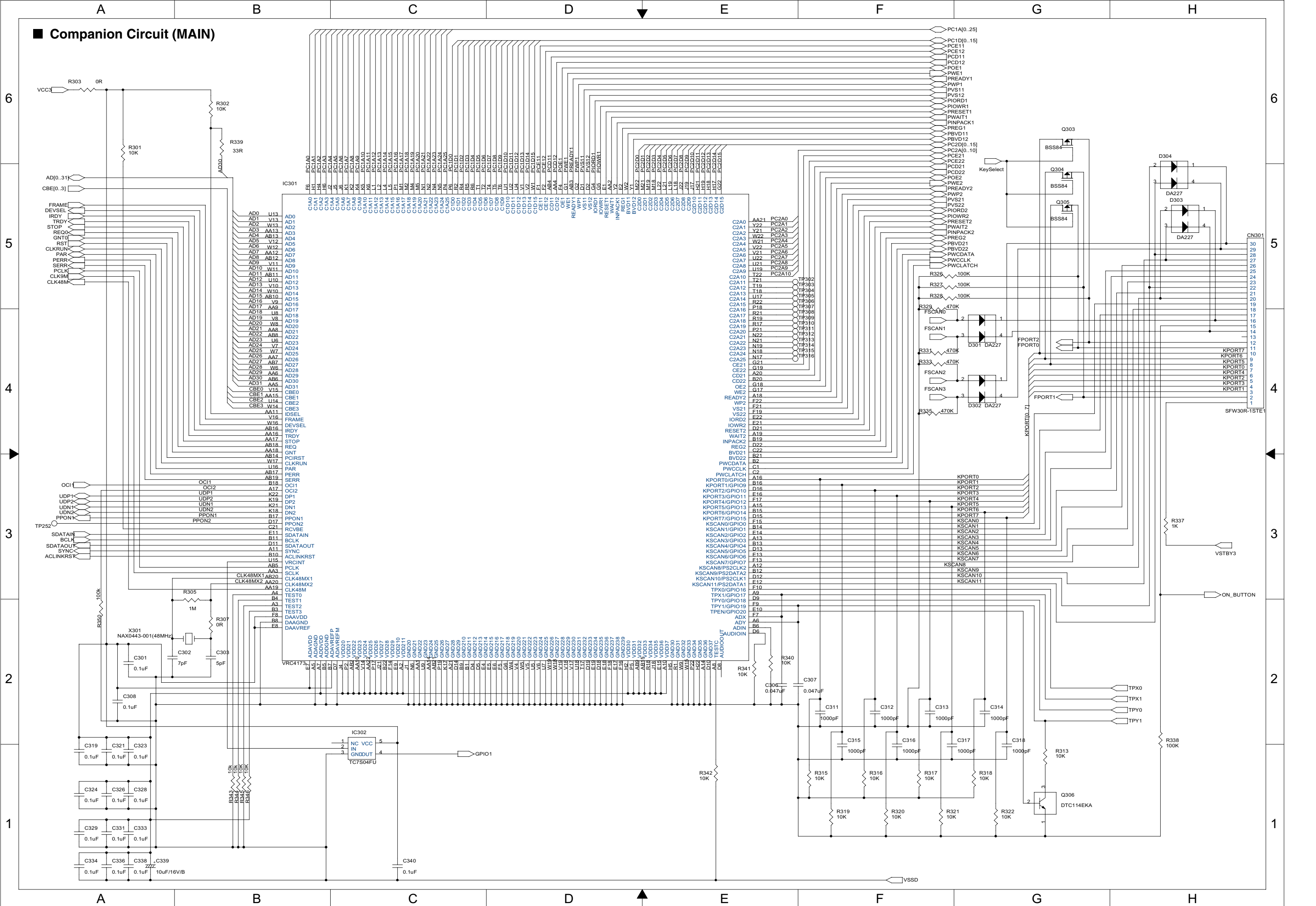
RAM CONNECTER Circuit (MAIN)



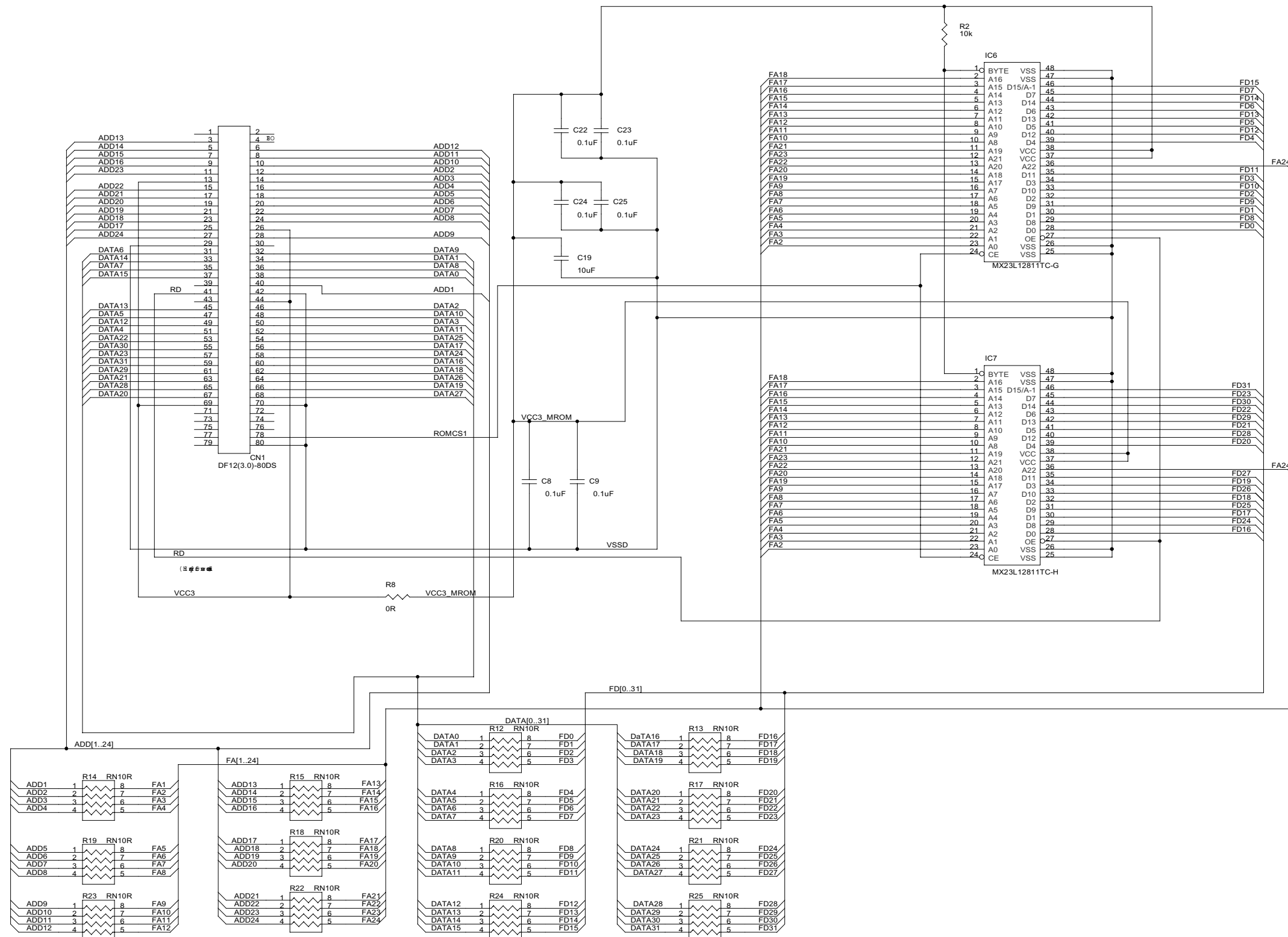
ROM CONNECTER Circuit (MAIN)



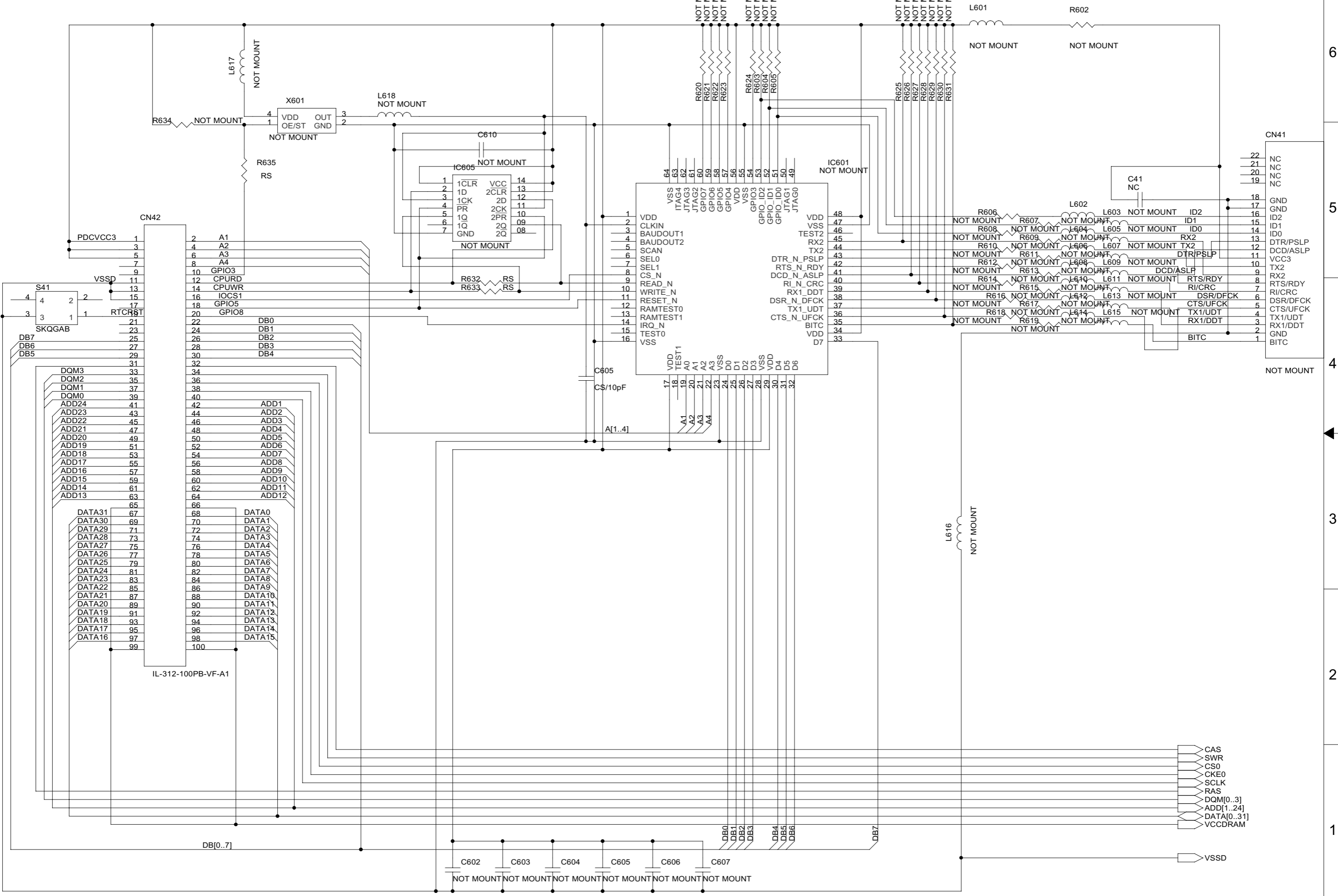
Companion Circuit (MAIN)



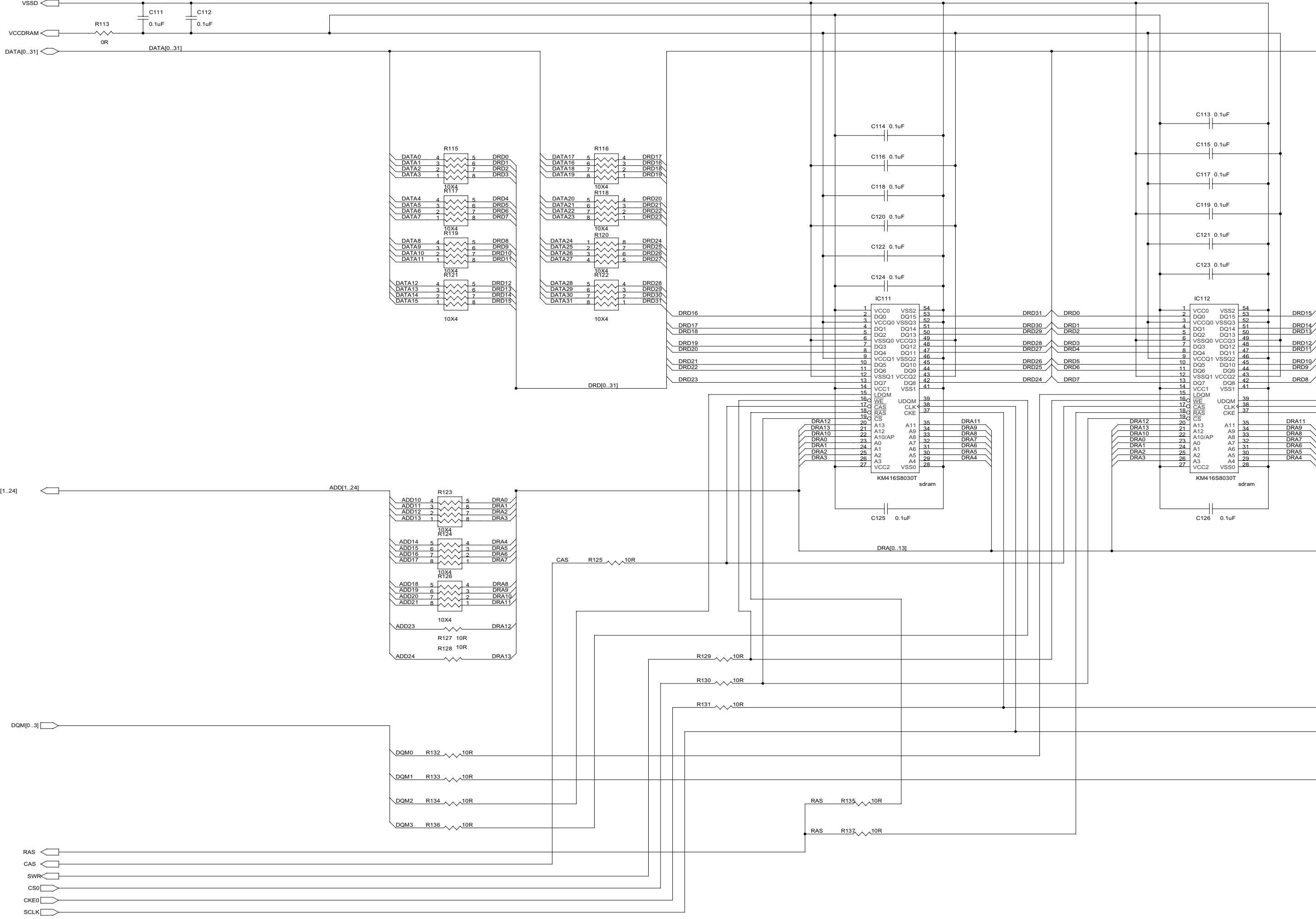
ROM Circuit (MAIN)



■ PDC Circuit (MAIN)



RAM Circuit (MAIN)



Circuit Boards

■ MOTHER BOARD (TOP)

